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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 1

### Complete if Known

Application Number	10/664,977
Filing Date	9/22/2003
First Named Inventor	Koji HOSONO et al.
Group Art Unit	2824
Examiner Name	Nguyen, V.
Attorney Docket Number	002372.00046

### U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
VTN		5701268		Lee et al.	12-23-1997	
VTN		5748536		Kwon et al.	05-05-1998	
VTN		4547685		Wong	10-15-1985	

### FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sub>2</sub>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
VTN		JP	57-152586	A	Nippon Denki KK	09-20-1982		Abst
VTN		JP	57-191890	A	Nippon Denki KK	11-25-1982		Abst

### OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>

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		Number	Kind Code <sup>2</sup> (if known)			
		4,547,685		Wong	10-15-1985	duplicate w/IDS filed 10/25/04
VTN		5,521,869		Ishimura et al.	05-28-1996	
		5,701,268		Lee et al.	12-23-1997	
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		5,163,021		Mehrotra		
VTN		5,602,789		Endoh		

**FOREIGN PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sub>0</sub>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
VTN		WO	95/34074	A	Intel	12-14-1995		
		JP	57-152586	A	Nippon Denki	09-20-1982		Abst
		JP	57-191890	A	Nippon Denki	11-25-1982		Abst
		JP	10-011982		Hiromi	01-1998		Abst
VTN		JP	10-092186		Masataka et al.	04-1998		Abst

**OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
VTN		Takeuchi et al., "Multipage Cell Architecture for High-Speed Programming Multilevel NAND Flash Memories," <i>IEEE J. Solid-State Circuits</i> , Vo. 33, No. 8, Aug. 1998, pp. 1228-1238	

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